



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY

HYDERABAD, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM No. **C 331427** BACHELOR OF TECHNOLOGY -- ELECTRICAL & ELECTRONICS ENGINEERING

Serial No. **680458219360**
Name **U SRINIVAS**

Name of the College: **E C E T, SIDDIPET**
Month & Year of Final Exam: **April, 2007**

Hall Ticket No. **03C71A0217** Year of Admission **2003-2004**

Class Awarded: ***** SECOND CLASS *****

S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS
	THEORY	20	80	100			LAB	25	50	75	

I YEAR

1	ENGLISH	19	49	68	6	2	MATHEMATICS - I	19	41	60	6
3	SOLID STATE PHYSICS	19	48	67	4	4	INFORMATION TECH. & NUME. METHODS	20	28	48	6
5	C & DATA STRUCTURES	19	31	50	6	6	NETWORK THEORY	19	71	90	6
7	ELECTRONICS DEVICES & CIRCUITS	17	35	52	6	8	ENGINEERING DRAWING PRACTICE	22	27	49	4
9	COMPUTER PROGRAMMING (LAB)	23	45	68	8	10	ELECTRONICS DEVICES & CKTS (LAB)	22	45	67	4

I SEMESTER

II YEAR

II SEMESTER

1	MATHEMATICS - II	18	28	46	4	1	MANAGERIAL ECONOMICS & FINANCIAL A	16	33	49	4
2	HYDRAULICS & HYDRAULIC MACHINERY	19	40	59	4	2	MATHEMATICS - III	17	28	45	4
3	PULSE & DIGITAL CIRCUITS	17	28	45	4	3	LINEAR & DIGITAL IC APPLICATIONS	15	41	56	4
4	SWITCHING THEORY & LOGIC DESIGN	16	40	56	4	4	POWER SYSTEMS - I	17	28	45	4
5	ELECTROMAGNETIC FIELDS	17	42	59	4	5	ELECTROMECHANICS - II	15	28	43	4
6	ELECTRO MECHANICS - I	16	37	53	4	6	CONTROL SYSTEMS	16	35	51	4
7	HYDRAULICS & HYDRAULIC MACHINERY (24	46	70	2	7	IC & PULSE & DIGITAL CIRCUITS (LAB)	17	38	55	2
8	NETWORKS (LAB)	22	46	68	2	8	ELECTROMECHANICS (LAB) - I	23	37	60	2

I SEMESTER

III YEAR

II SEMESTER

1	COMPUTER ORGANIZATION	17	28	45	4	1	ANALYSIS OF LINEAR SYSTEMS	16	28	44	4
2	ELECTRICAL MEASUREMENTS	17	29	46	4	2	MICROPROCESSORS AND INTERFACING	13	28	41	4
3	POWER SYSTEMS II	18	28	46	4	3	MODELLING OF POWER SYSTEM COMPONENTS	16	30	46	4
4	POWER ELECTRONICS	17	37	54	4	4	INSTRUMENTATION	17	30	47	4
5	ELECTROMECHANICS III	16	28	44	4	5	POWER SYSTEMS - II	18	30	48	4
6	MANAGEMENT SCIENCE	18	34	52	4	6	UTILIZATION OF ELECTRICAL ENERGY	17	28	45	4
7	ELECTROMECHANICS (LAB) - II	24	44	68	2	7	ELECTRICAL MEASUREMENTS LAB	23	43	66	2
8	CONTROL SYSTEMS (LAB)	23	44	67	2	8	POWER ELECTRONICS LAB	22	40	62	2

I SEMESTER

IV YEAR

II SEMESTER

1	DIGITAL SIGNAL PROCESSING	16	31	47	4	1	H V D C TRANSMISSION	19	29	48	4
2	POWER SEMICONDUCTOR DRIVES	15	37	52	4	2	DATABASE MANAGEMENT SYSTEMS	20	28	48	
3	COMPUTER METHODS IN POWER SYS.	17	36	53	4	3	PROJECT & SEMINAR #	38	138	176	8
4	POWER SYSTEM OPERATION & CONTROL	16	47	63	4						
5	OPTIMIZATION TECHNIQUES	18	40	58	4						
6	ELECTRICAL DISTRIBUTION SYS.	15	34	49	4						
7	MICROPROCESSORS LAB	21	39	60	2						
8	SIMULATION OF ELEC. SYS. LAB	22	44	66	2						

Number of Credits registered for: **212**
Aggregate Marks Secured for best:

(# Project Internal = 40, External = 160)

212 CREDITS TOTAL 3020 OUT OF 5075 (59.51 %)

Date of Declaration of Result: **May 18, 2007**
(See overleaf for instructions)

(*Courses registered but not counted for calculation of aggregate)

[Signature]
CONTROLLER OF EXAMINATIONS