

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

ANANTHAPURAMU - 515 002, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET

CMM. No.

JAC 165889

Serial No.

K S JAYA PRAKASH

Name Course

ELECTRONICS & COMMUNICATION ENGINEERING of Admission 2011

Name of the College SITAMS-CHITTOOR *** Second Class ***

Ž COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	Ž COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	
				YE	AR				-
ENGLISH ENGINEERING PHYSICS ENGINEERING CHEMISTRY MATHEMATICS-I PROGRAMMING IN C AND DATA STRUCTURES	21 28 21 22 18	49 45 33 40 25	70 73 54 62 43	4 4 6 6	ENGINEERING DRAWING MATHEMATICAL METHODS C PROGRAMMING AND DATA STRUCTURES LAB ENGINEERING AND I.T. WORKSHOP DENGINEERING PHYSICS AND ENGINEERING CHEMISTRY LAB 1 ENGLISH LANGUAGE AND COMMUNICATION SKILLS LAB	7 17 16 10 22 18	39 49 24 31 41	46 66 40 41 63 58	6 6 4 4 4 4
I SEMESTER				I YI	EAR II SEMESTER				1
1 MATHEMATICS-III 2 ENVIRONMENTAL SCIENCE 3 ELECTRICAL CIRCUITS 4 PROBABILITY THEORY AND STOCHASTIC PROCESSES 5 ELECTRONIC DEVICES AND CIRCUITS 6 SIGNALS AND SYSTEMS 7 ELECTRONIC DEVICES AND CIRCUITS LAB 8 BASIC SIMULATION LAB	22 10 14 15 12 15 19 18	39 30 29 37 28 30 38 25	61 40 43 52 40 45 57 43	4 4 4 4 2 2	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS PRINCIPLES OF ELECTRICAL ENGINEERING ELECTRONIC CIRCUIT ANALYSIS PULSE AND DIGITAL CIRCUITS SWITCHING THEORY AND LOGIC DESIGN ELECTROMAGNETIC THEORY AND TRANSMISSION LINES ELECTRONIC CIRCUIT ANALYSIS LAB ELECTRICAL ENGINEERING LAB	17 17 20 17 17 15 19 18	25 29 27 28 25 25 25 42 22	42 46 47 45 42 40 61 40	4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4
I SEMESTER				II Y	EAR II SEMESTER				
1 CONTROL SYSTEMS 2 ANALOG COMMUNICATIONS 3 LINEAR IC APPLICATIONS 4 ANTENNAS AND WAVE PROPAGATION 5 COMPUTER ORGANIZATION 6 DIGITAL IC APPLICATIONS 7 LINEAR AND DIGITAL IC APPLICATIONS LAB 8 PULSE AND DIGITAL CIRCUITS LAB	19 15 21 19 20 17 18 21	42 32 27 35 27 38 37 45	61 47 48 54 47 55 55 66	4 4 4 4 4 4 2 2	DIGITAL COMMUNICATIONS MICROPROCESSORS AND MICROCONTROLLERS DIGITAL SIGNAL PROCESSING ELECTRONIC MEASUREMENTS AND INSTRUMENTATION VLSI DESIGN MICROWAVE ENGINEERING ANALOG AND DIGITAL COMMUNICATIONS LAB ADVANCED ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	20 29 17 24 20 27 18 22	28 15 29 27 26 11 42 44	48 44 46 51 46 38 60 66	
I SEMESTER	1			IV Y	EAR II SEMESTER				
T MANAGEMENT SCIENCE 2 EMBEDDED REAL TIME OPERATING SYSTEMS 3 COMPUTER NETWORKS 4 OPTICAL COMMUNICATIONS 5 RADAR SYSTEMS 6 DIGITAL DESIGN THROUGH VERILOG HDL 7 MICROWAVE AND OPTICAL COMMUNICATIONS LAB 8 MICROPROCESSORS AND DSP LAB	18 25 15 19 24 17 22 23	25 26 25 31 32 36 46	43 51 40 50 56 53 68 53	4 4 4 4 2 2	CELLULAR AND MOBILE COMMUNICATIONS DIGITAL IMAGE PROCESSING SATELLITE COMMUNICATIONS WIRELESS COMMUNICATIONS AND NETWORKS SEMINAR PROJECT WORK	23 12 15 17 38 48	25 28 29 29 - 135	48 40 44 46 38 183	

Number of Credits registered for 220 Total Credits Acquired

Aggregate Marks Secured for best 2923

Percentage of Marks 56.21 % April 2016 CMM Issue Date

(See overleaf for Instructions)

5200 Max.Marks

CONTROLLER OF EXAMINATION

(*Courses registered but not counted for calculation of aggregate)

AWARD OF CLASS

1st Class with Distinction : 70% or more

1st Class : Below 70% but not less than 60% 2nd Class : Below 60% but not less than 50% Pass Class : Below 50% but not less than 40%

- Note: (i) A student shall be deemed to have satisfied the minimum academic requirements and earned the credits allotted to each theory or practical design or drawing subject or project if he/she secures not less than 35% of marks in the end examination and a minimum of 40% of marks in the sum total of the internal evaluation and the end examination taken together.
 - (ii) For lateral entry students the course is of three years duration and they are directly admitted into II year of the four year B.Tech/ B. Pharmacy Degree course.

(Any discrepancy in the entries noted in CMM must be brought to the notice of the Controller of Examinations through College Principal within **One Month** from the issued date. This document is issued without any correction or overwriting.)