



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY  
HYDERABAD, ANDHRA PRADESH, INDIA**

**CONSOLIDATED MARKS MEMO / CREDIT SHEET**



U. No.: **C 353746** BACHELOR OF TECHNOLOGY -- ELECTRICAL & ELECTRONICS ENGINEERING

Name of the College: **GIET, RAJAHMUNDRY**  
Month & Year of Final Exam: **May, 2008**

523408193398  
**NARASIMHARAO MARISETTI**

Ticket No.: **04551A0230** Year of Admission: **2004-2005** Class Awarded: **FIRST CLASS WITH DISTINCTION**

| COURSE TITLE | INT. MARKS | EXT. MARKS | TOTAL | CREDITS | S.No | COURSE TITLE | INT. MARKS | EXT. MARKS | TOTAL | CREDITS |
|--------------|------------|------------|-------|---------|------|--------------|------------|------------|-------|---------|
| THEORY       | 20         | 80         | 100   |         |      | LAB          | 25         | 50         | 75    |         |

**I YEAR**

|                                |    |    |    |   |    |                                  |    |    |    |   |
|--------------------------------|----|----|----|---|----|----------------------------------|----|----|----|---|
| ENGLISH                        | 18 | 45 | 63 | 6 | 2  | MATHEMATICS - I                  | 18 | 68 | 86 | 6 |
| SOLID STATE PHYSICS            | 17 | 35 | 52 | 4 | 4  | INFORMATION TECH.& NUME. METHODS | 18 | 52 | 70 | 6 |
| C & DATA STRUCTURES            | 16 | 34 | 50 | 6 | 6  | NETWORK THEORY                   | 19 | 62 | 81 | 6 |
| ELECTRONICS DEVICES & CIRCUITS | 17 | 52 | 69 | 6 | 8  | ENGINEERING DRAWING PRACTICE     | 22 | 47 | 69 | 4 |
| COMPUTER PROGRAMMING (LAB)     | 19 | 40 | 59 | 8 | 10 | ELECTRONICS DEVICES & CKTS (LAB) | 19 | 40 | 59 | 4 |

**I SEMESTER**

**II YEAR**

**II SEMESTER**

|  |    |    |    |   |   |                                     |    |    |    |   |
|--|----|----|----|---|---|-------------------------------------|----|----|----|---|
| MATHEMATICS - II                       | 19 | 79 | 98 | 4 | 1 | MANAGERIAL ECONOMICS & FINANCIAL A  | 18 | 47 | 65 | 4 |
| HYDRAULICS & HYDRAULIC MACHINERY       | 19 | 63 | 82 | 4 | 2 | MATHEMATICS - III                   | 17 | 69 | 86 | 4 |
| PULSE & DIGITAL CIRCUITS               | 17 | 33 | 50 | 4 | 3 | LINEAR & DIGITAL IC APPLICATIONS    | 18 | 48 | 66 | 4 |
| SWITCHING THEORY & LOGIC DESIGN        | 17 | 70 | 87 | 4 | 4 | POWER SYSTEMS - I                   | 16 | 60 | 76 | 4 |
| ELECTROMAGNETIC FIELDS                 | 17 | 66 | 83 | 4 | 5 | ELECTROMECHANICS - II               | 16 | 56 | 72 | 4 |
| ELECTROMECHANICS - I                   | 17 | 59 | 76 | 4 | 6 | CONTROL SYSTEMS                     | 17 | 71 | 88 | 4 |
| HYDRAULICS & HYDRAULIC MACHINERY (LAB) | 24 | 45 | 69 | 2 | 7 | IC & PULSE & DIGITAL CIRCUITS (LAB) | 24 | 47 | 71 | 2 |
| NETWORKS (LAB)                         | 23 | 39 | 62 | 2 | 8 | ELECTROMECHANICS (LAB) - I          | 24 | 46 | 70 | 2 |

**I SEMESTER**

**III YEAR**

**II SEMESTER**

|                             |    |    |    |   |   |                                      |    |    |    |   |
|-----------------------------|----|----|----|---|---|--------------------------------------|----|----|----|---|
| COMPUTER ORGANIZATION       | 19 | 38 | 57 | 4 | 1 | ANALYSIS OF LINEAR SYSTEMS           | 18 | 61 | 79 | 4 |
| ELECTRICAL MEASUREMENTS     | 16 | 65 | 81 | 4 | 2 | MICROPROCESSORS AND INTERFACING      | 16 | 34 | 50 | 4 |
| POWER SYSTEMS-II            | 18 | 52 | 70 | 4 | 3 | MODELLING OF POWER SYSTEM COMPONENTS | 18 | 67 | 85 | 4 |
| POWER ELECTRONICS           | 19 | 55 | 74 | 4 | 4 | INSTRUMENTATION                      | 17 | 51 | 68 | 4 |
| ELECTROMECHANICS-III        | 17 | 62 | 79 | 4 | 5 | POWER SYSTEMS - III                  | 17 | 52 | 69 | 4 |
| MANAGEMENT SCIENCE          | 15 | 38 | 53 | 4 | 6 | UTILIZATION OF ELECTRICAL ENERGY     | 17 | 43 | 60 | 4 |
| ELECTROMECHANICS (LAB) - II | 25 | 49 | 74 | 2 | 7 | ELECTRICAL MEASUREMENTS LAB          | 23 | 43 | 66 | 2 |
| CONTROL SYSTEMS (LAB)       | 24 | 47 | 71 | 2 | 8 | POWER ELECTRONICS LAB                | 20 | 34 | 54 | 2 |

**I SEMESTER**

**IV YEAR**

**II SEMESTER**

|                                  |    |    |    |   |   |                             |    |     |     |   |
|----------------------------------|----|----|----|---|---|-----------------------------|----|-----|-----|---|
| DIGITAL SIGNAL PROCESSING        | 18 | 56 | 74 | 4 | 1 | H V D C TRANSMISSION        | 19 | 48  | 67  | 4 |
| POWER SEMICONDUCTOR DRIVES       | 16 | 52 | 68 | 4 | 2 | DATABASE MANAGEMENT SYSTEMS | 18 | 41  | 59  | 4 |
| COMPUTER METHODS IN POWER SYS.   | 16 | 51 | 67 | 4 | 3 | PROJECT & SEMINAR #         | 36 | 153 | 189 | 8 |
| POWER SYSTEM OPERATION & CONTROL | 19 | 55 | 74 | 4 |   |                             |    |     |     |   |
| H V ENGINEERING                  | 14 | 34 | 48 | 4 |   |                             |    |     |     |   |
| ELECTRICAL DISTRIBUTION SYS.     | 15 | 69 | 84 | 4 |   |                             |    |     |     |   |
| MICROPROCESSORS LAB              | 24 | 46 | 70 | 2 |   |                             |    |     |     |   |
| SIMULATION OF ELEC. SYS. LAB     | 22 | 42 | 64 | 2 |   |                             |    |     |     |   |

Number of Credits registered for: **212** (#Project Internal = 40, External = 160)  
Aggregate Marks Secured for best: **212 CREDITS TOTAL 3813 OUT OF 5075 (75.13 %)**

*B. Deep Kumar*

Date of Declaration of Result: **May 16, 2008** (\*Courses registered but not counted for calculation of aggregate)

**CONTROLLER OF EXAMINATIONS**