

Serial No. 0133218

UTTARAKHAND TECHNICAL UNIVERSITY, DEHRADUN (INDIA)

STATEMENT OF MARKS 2010-11

S.No: 141204

B.Tech (Electrical & Electronics Engineering)

FINAL YEAR



Name of Candidate : Akashat Vashisth
 Father's Name : Tribhuvan C. Pandey
 Name of Institution : Uttaranchal Institute of Technology Dehradun

Roll No.: 07300108004

Enrollment No.: 07300108351

7th Semester							8th Semester						
Subject Code and Name	Maximum Marks			Marks Obtained			Subject Code and Name	Maximum Marks			Marks Obtained		
	Exam	Sess	Total	Exam	Sess	Total		Exam	Sess	Total	Exam	Sess	Total
THEORY							THEORY						
TEC 704 Digital Signal Processing	100	50	150	43	35	78	TEC 803 Telemetry & Data Transmission	100	50	150	46	44	90
TEE 011 Utilization of Electric Energy & Traction	100	50	150	54	43	97	TEE 025 High Voltage AC and DC Transmission *	100	50	150	58	44	102
TEE 702 Electric Drives	100	50	150	50	45	95	TEE 032 Power Converter Application	100	50	150	49	43	92
TEE 704 Switch Gear & Protection	100	50	150	49	45	94	TEE 803 Operation and Control in Power System	100	50	150	37	42	79
TDE 018 Consumer Electronics	100	50	150	43	40	83							
PRACTICAL							PRACTICAL						
PEC 755 Digital Signal Processing Lab	25	25	50	20	17	37	PEC 853 Telemetry Lab	25	25	50	19	22	41
PEE 751 Power System Lab	25	25	50	18	18	36	PEE 852 Project	200	100	300	175	94	269
PEE 753 Industrial Training & Seminar	--	50	50	--	47	47	GP General Proficiency	--	100	100	--	94	94
PEE 754 Project	--	50	50	--	47	47							
TOTAL	550	400	950	277	337	614	TOTAL	625	425	1050	384	383	767

Carry Over Paper(s)		Grace Marks (if any)	Marks Obtained/Maximum Marks				Grand Total	Final Result
Total No.	Subject(s) Code		First Year (25%)	Second Year (50%)	Third Year (75%)	Fourth Year (100%)		
			321/500	588/1000	923/1500	1381/2000	3213/5000	First Div

Dated: 25/11/2011

Prepared By: *Sanyal*

Checked By: *Sanyal*

Sanyal
 Deputy Registrar
 Uttarakhand Technical University
 Dehradun
 Registrar

Serial No.0058843

UTTARAKHAND TECHNICAL UNIVERSITY, DEHRADUN (INDIA)



STATEMENT OF MARKS 2009-10

S.No: 101027

B.Tech (Electrical & Electronics Engineering) THIRD YEAR

Name of Candidate : Akashat Vashieth
 Father's Name : Tribhuvan C. Pandey
 Name of Institution : Uttaranchal Institute of Technology Dehradun

Roll No.: 07300108004

Enrollment No.: 07300108351

5th Semester							6th Semester						
Subject Code and Name	Maximum Marks			Marks Obtained			Subject Code and Name	Maximum Marks			Marks Obtained		
	Exam	Sess	Total	Exam	Sess	Total		Exam	Sess	Total	Exam	Sess	Total
THEORY							THEORY						
TEC 507 Analog Integrated Electronics *	100	50	150	43	38	81	TEC 606 Analog and Digital Communication Engg.	100	50	150	32	40	72
TEE 501 Electromechanical Energy Conversion-II	100	50	150	48	35	83	TEE 601 Power System Analysis	100	50	150	44	37	81
TEE 502 Control System	100	50	150	30	47	77	TEE 603 Power Electronics	100	50	150	37	40	77
TEE 503 Elements of Power System	100	50	150	53	41	94	TEE 606 High Voltage Engineering	100	50	150	34	44	78
THU 501 Industrial Economics & Principle of Management	100	50	150	42	40	82	TEE 607 Applied Instrumentation	100	50	150	47	40	87
PRACTICAL							PRACTICAL						
PEC 557 Electronics Lab -II	25	25	50	19	22	41	PEE 651 Power Electronics Lab	25	25	50	22	18	40
PEE 551 Electromechanical Energy Conversion-II Lab	25	25	50	21	21	42	PEE 655 Transducers Lab	25	25	50	22	14	36
PEE 552 Control System Lab	25	25	50	18	21	39	TEN 651 Seminar-I Discipline	--	50	50	--	40	40
							GP General Proficiency	--	100	100	--	90	90
								--	100	100	--	90	90
TOTAL	575	325	900	274	265	539	TOTAL	550	550	1100	238	453	691

Carry Over Paper(s) (if any)		Grace Marks (if any)	Marks Obtained/Maximum Marks			Result
Total No.	Subject(s) Code		5th Semester	6th Semester	Grand Total	
			539/900	691/1100	1230/2000	Pass

Dated: 20/05/2011

Prepared By: *PS*

Checked By: *Akashat Vashieth*

PS
 Registrar
 Uttarakhand Technical University
 Dehradun
 Registrar

UTTARAKHAND TECHNICAL UNIVERSITY, DEHRADUN (INDIA)



STATEMENT OF MARKS 2008-09

S.No: 003007

B.Tech (Electrical & Electronics Engineering)

SECOND YEAR

Name of Candidate : Akashat Vashieth
 Father's Name : Tribhuvan C. Pandey
 Name of Institution : Uttaranchal Institute of Technology Dehradun

Roll No.: 07300108004
 Enrollment No.: 07300108351

3rd Semester						4th Semester											
Subject Code and Name	Maximum Marks			Marks Obtained			Subject Code and Name	Maximum Marks			Marks Obtained						
	Exam	Sees	Total	Exam	Sees	Total		Exam	Sees	Total	Exam	Sees	Total				
THEORY						THEORY											
TEC 301 Solid State Devices and Circuits	100	50	150	46	36	82	TEC 401 Electromagnetic Field Theory	100	50	150	44	30	74				
TEC 304 Pulse & Digital Electronics	100	50	150	34	40	74	TEE 401 Electromechanical Energy Conversion - I	100	50	150	31	40	71				
TEE 301 Network Analysis & Synthesis	100	50	150	47	39	86	TEE 402 Power Station Practice	100	50	150	25	42	67				
TEE 302 Electrical Measurements and Measuring Instruments	100	50	150	31	40	71	TEE 403 Electrical & Electronics Engg Materials	100	50	150	34	33	67				
TMA 301 Mathematics -III	100	50	150	33	40	73	TEE 404 Microprocessor	100	50	150	42	42	84				
PRACTICAL						PRACTICAL											
PEC 353 Analog & Digital Electronics lab-I	25	25	50	21	22	43	PEE 451 Electromechanical Energy Conversion -I Lab.	25	25	50	20	18	38				
PEE 351 Network Lab	25	25	50	23	21	44	PEE 453 Microprocessor Lab.	25	25	50	18	21	39				
PEE 352 Electrical Measurement Lab.	25	25	50	19	20	39	PEE 453 Electrical Simulation Lab	25	25	50	21	21	42				
							Disipline	--	100	100	--	92	92				
							GP	--	100	100	--	89	89				
							General Proficiency										
TOTAL			575	325	900	254	258	512	TOTAL			575	525	1100	235	428	663

Carry Over Paper(s) (if any)		Grace Marks (if any)	Marks Obtained/Maximum Marks			Result
Total No.	Subject(s) Code		3rd Semester	4th Semester	Grand Total	
		5	512/900	663/1100	1175/2000	Pass with Grace

Dated: 16/01/2010

Prepared By:

Singh
Checked By:

Munish
Registrar
Uttarakhand Technical University
Dehradun
Registrar

UTTARAKHAND TECHNICAL UNIVERSITY, DEHRADUN (INDIA)



STATEMENT OF MARKS 2007-08

S.No: 075672

B.Tech (Electrical & Electronics Engineering)
FIRST YEAR

Name of Candidate : Akashat Vashisti
Father's Name : Tribhuvan Chandra Pandey
Name of Institution : Uttaranchal Institute of Technology Dehradun

Roll No.: 07300108004
Enrollment No.: 07300108351

1st Semester						2nd Semester							
Subject Code and Name	Maximum Marks			Marks Obtained			Subject Code and Name	Maximum Marks			Marks Obtained		
	Exam	Sess	Total	Exam	Sess	Total		Exam	Sess	Total	Exam	Sess	Total
THEORY						THEORY							
TCY 101 Chemistry	100	50	150	73	37	110	TCS 201 Computer System Programming	100	50	150	70	40	110
TEC 101 Basic Electronics	100	50	150	41	32	73	TES 201 Environmental Studies	50	25	75	25	20	45
TEE 101 Basic Electrical Engineering	100	50	150	50	36	86	TTH 201 Technical Writing	50	25	75	17	20	37
THM 101 Professional Communication	50	25	75	29	21	50	TMA 201 Mathematics *	100	50	150	30	34	64
TMA 101 Mathematics	100	50	150	59	32	91	TME 201 Mechanical Engg *	100	50	150	30	32	62
PRACTICAL						PRACTICAL							
POG 151 Engineering Graphics	50	25	75	46	21	67	PCS 251 Computer Lab	25	25	50	23	22	45
PCY 151 Chemistry Lab	25	25	50	20	16	36	PME 251 Mechanical Engineering Lab	25	25	50	20	22	42
PEE 151 Basic Electrical & Electronics Lab	25	25	50	23	19	42	PPH 251 Physics Lab	25	25	50	18	17	35
PWS 151 Workshop Practice	25	25	50	22	21	43	Discipline	--	100	100	--	88	88
TOTAL	575	325	900	363	235	598	TOTAL	575	525	1100	271	413	684

Carry Over Paper(s) (if any)		Grace Marks (if any)	Marks Obtained/Maximum Marks			Result
Total No.	Subject(s) Code		1st Semester	2nd Semester	Grand Total	
			598/900	684/1100	1282/2000	Pass

Dated: 22/01/2011

Prepared By: *[Signature]*

Checked By: *[Signature]*

[Signature]
Registrar
Uttarakhand Technical University
Dehradun