



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

KAKINADA - 533 003, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM No. **K00053549**

Department of Technology: Electrical and Electronics Engineering

Serial No.: 201007008952

Name of the College: University College of Engineering

Name: **CHINTA L N S DHANARAJU**

Name & Year of Final Exam: B.Tech 2011

Hall Ticket No: 07021A0246

Year of Admission: 2007 - 2008

Class Awarded: First Class with Distinction

S. No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S. No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS
I YEAR											
1	APPLIED PHYSICS	17	74	91	4	2	C PROGRAMMING AND DATA STRUCTURES	19	60	79	6
3	ELECTRICAL CIRCUITS	15	58	73	4	4	ELECTRONIC DEVICES AND CIRCUITS	17	52	69	6
5	ENGLISH	17	41	58	4	6	MATHEMATICAL METHODS	20	69	89	6
7	MATHEMATICS-I	18	68	86	6	8	COMPUTER PROGRAMMING LAB	24	42	66	4
9	ELECTRONIC DEVICES AND CIRCUITS LAB	22	38	60	4	10	ENG WORKSHOP PRACTICE/IT WORKSHOP	23	43	66	4
11	ENGINEERING DRAWING PRACTICE LAB	25	50	75	4	12	ENGLISH LANG COMMUNICATION SKILLS	23	45	66	4
II YEAR											
1	ELECTRICAL MACHINES-I	18	40	58	4	1	CONTROL SYSTEMS	18	52	70	4
2	ELECTROMAGNETIC FIELDS	15	52	67	4	2	ELECTRICAL MACHINES - II	16	54	70	4
3	FLUID MECHANICS & HYDRAULIC MACHINE	17	47	64	4	3	ENVIRONMENTAL STUDIES	16	61	77	4
4	MATHEMATICS-III	15	58	73	4	4	LINEAR AND DIGITAL IC APPLICATIONS	18	30	48	4
5	PULSE AND DIGITAL CIRCUITS	20	62	82	4	5	MANAGERIAL ECONOMICS & FINANCIAL AN	18	60	78	4
6	SWITCHING THEORY AND LOGIC DESIGN	20	49	69	4	6	POWER SYSTEMS - I	17	40	57	4
7	ELECTRICAL CIRCUITS LAB	22	43	65	2	7	ELECTRICAL MACHINES LAB - I	23	71	94	2
8	FLUID MECHANICS & HYDRAULIC MACHINES	22	35	57	2	8	IC AND PULSE & DIGITAL CIRCUITS LAB	24	47	71	2
III YEAR											
1	ADVANCED NETWORK THEORY	18	62	80	4	1	DIGITAL SIGNAL PROCESSING	18	67	85	4
2	COMPUTER ORGANIZATION	17	48	65	4	2	INSTRUMENTATION	16	50	66	4
3	ELECTRICAL MACHINES-III	15	59	74	4	3	MANAGEMENT SCIENCE	17	62	79	4
4	ELECTRICAL MEASUREMENTS	15	45	60	4	4	MICROPROCESSORS AND MICROCONTROL	17	37	54	4
5	POWER ELECTRONICS	15	36	51	4	5	SWITCHGEAR AND PROTECTION	17	47	64	4
6	POWER SYSTEMS - II	17	55	72	4	6	VLSI DESIGN	18	51	69	4
7	CONTROL SYSTEMS LAB	21	42	63	2	7	ADVANCED ENGLISH COMMUNI. SKILLS LAB	22	39	61	2
8	ELECTRICAL MACHINES LAB-II	22	43	65	2	8	POWER ELECTRONICS LAB	24	49	73	2
IV YEAR											
1	ELECTRICAL DISTRIBUTION SYSTEMS	18	56	74	4	1	COMPREHENSIVE VIVA	0	92	92	1
2	HIGH VOLTAGE ENGINEERING	18	55	73	4	2	DATABASE MANAGEMENT SYSTEMS	17	32	49	4
3	NEURAL NETWORKS & FUZZY LOGIC	12	63	75	4	3	OPTIMIZATION TECHNIQUES	16	60	76	4
4	POWER SEMICONDUCTOR DRIVES	17	49	66	4	4	UTILIZATION OF ELECTRICAL ENGINEER	19	63	82	4
5	POWER SYSTEM ANALYSIS	16	59	75	4	5	SEMINAR	0	46	46	1
6	POWER SYSTEM OPERATION AND CONTROL	14	36	50	4	6	IND ORIENTED MINI PROJECT	0	41	41	1
7	ELECTRICAL MEASUREMENTS LAB	13	45	58	2	7	PROJECT WORK	37	156	193	1
8	MICROPROCESSORS & MICROCONTROLLER	12	44	56	2						

Number of Credits registered for: 224

Aggregate Marks Secured for best: 216 Credits 4972 out of 5325 (76.47%)

Date of Declaration of Result:

May 2011

(See overleaf for Instructions)

(Courses registered but not counted for calculation of aggregate)

10/8/2011

CONTROLLER OF EXAMINATIONS