

Anna University




Reg. No.42506106030/RG

The Syndicate of the Anna University hereby makes known that **VENKATESAN A** has been admitted to the **DEGREE OF BACHELOR OF ENGINEERING** in **ELECTRONICS AND COMMUNICATION ENGINEERING** under the Faculty of Information and Communication Engineering, having completed the prescribed programme of study and having been certified by the duly appointed examiners to be qualified to receive the same, and has been placed in **FIRST CLASS** at the Examination held in **NOVEMBER 2010**.

Given under the Seal of the University



Chennai 600 025
India
July 2011
CBU/14/2011/0214


Controller of Examinations


Registrar


Vice-Chancellor
UNIVERSITY DEVELOPMENT

ANNA UNIVERSITY CHENNAI, CHENNAI - 600 025
B.E.-DEGREE EXAMINATIONS
CONSOLIDATED STATEMENT OF MARKS



NAME OF THE CANDIDATE		VENKATESAN A		REGISTER NO.	42306106030	
COLLEGE OF STUDY		425, Sri Jayaram Engineering College		MONTH & YEAR OF LAST APPEARANCE	November 2010	
PROGRAMME & BRANCH		B.E. Electronics and Communication Engineering		REGULATIONS	2004	
SEMI	SUBJECT CODE	SUBJECT TITLE	MAX	MIN	MARKS SECURED	MONTH & YEAR OF PASSING
01*	ET1201	Engineering Chemistry	100	50	061	APR 2007
01*	ET1201	Electron Devices	100	50	068	APR 2007
01*	ET1202	Circuit Analysis	100	50	056	APR 2007
01*	ET1201	Engineering Graphics	100	50	053	APR 2007
01*	ET1202	Computer Programming	100	50	055	APR 2007
01*	ET1201	Technical English	100	50	059	NOV 2007
01*	MA1201	Engineering Mathematics - I	100	50	055	NOV 2010
01*	ET1201	Engineering Physics	100	50	056	NOV 2007
01*	ET1203	Engineering Practices Laboratory	100	50	066	APR 2007
01*	ET1201	Physics and Chemistry Laboratory	100	50	079	APR 2007
03	CS1151	Data Structures	100	50	065	NOV 2007
03	ET1201	Environmental Science and Engineering	100	50	054	NOV 2007
03	ET1201	Digital Electronics	100	50	056	APR 2008
03	ET1203	Electronic Circuits - I	100	50	064	APR 2008
03	ET1211	Electrical Machines	100	50	055	NOV 2007
03	MA1201	Mathematics - III	100	50	063	APR 2010
03	CS1152	Data Structure Lab	100	50	092	NOV 2007
03	ET1204	Electronic Devices and Circuits Lab I	100	50	089	NOV 2008
03	ET1261	Electrical Machines Lab	100	50	089	NOV 2007
04	ET1251	Electronic Circuits II	100	50	059	NOV 2008
04	ET1252	Signals and Systems	100	50	074	NOV 2010
04	ET1253	Electromagnetic Fields	100	50	057	APR 2008
04	ET1254	Linear Integrated Circuits	100	50	056	APR 2009
04	ET1255	Measurements and Instrumentation	100	50	056	NOV 2010
04	MA1254	Random Processes	100	50	083	NOV 2008
04	ET1256	Electronic circuits II and simulation lab	100	50	089	APR 2008
04	ET1257	Linear Integrated Circuit Lab	100	50	089	APR 2008
04	ET1258	Digital Electronics lab	100	50	089	APR 2008
05	ET1301	Communication Theory	100	50	053	NOV 2008
05	ET1302	Digital Signal Processing	100	50	054	NOV 2008
05	ET1303	Microprocessors and Its Applications	100	50	052	NOV 2008
05	ET1304	Control Systems	100	50	052	NOV 2008
05	ET1305	Transmission Lines and Waveguides	100	50	053	NOV 2009
05	MA1251	Numerical Methods	100	50	065	NOV 2008
05	ET1306	Digital Signal Processing Lab	100	50	083	NOV 2008
05	ET1307	Microprocessor and Application Lab	100	50	055	APR 2009
06	CS1231	Computer Architecture	100	50	064	NOV 2010
06	CS1302	Computer Networks	100	50	051	APR 2009
06	ET1351	Digital Communication	100	50	051	NOV 2009
06	ET1352	Antenna and Wave Propagation	100	50	051	NOV 2008
06	MG1351	Principles of Management	100	50	055	APR 2009
06	ET1006	Medical Electronics	100	50	061	APR 2009
06	ET1353	Communication System Lab	100	50	087	APR 2009
06	ET1354	Networks Lab	100	50	089	APR 2009
06	ET1355	Electronic System Design Lab	100	50		

* Annual Pattern is followed in the First Year of study and Semester Pattern is followed from Second Year onwards.
End of Statement
Classification : FIRST CLASS
Total Marks from 3rd to 8th semesters : 3248 / 4960
Percentage (rounded to nearest integer) : 66



V. P. M.