



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

KAKINADA - 533 003 , ANDHRA PRADESH, INDIA

## CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM. No.: **K 00084036** Bachelor of Technology **Electrical and Electronics Engineering**

Serial No.: **201003026493**

Name of the College: **D VENKATA RAMANA & DR. HIMASEKAR**  
Name & Year of Final Exam: **MIC COLL. OF TECH.**

Name: **MATTAPARTHI MANIVISHNU**

**B.Tech 2013**

Hall Ticket No.: **09H71A0223** Year of Admission **2009 - 2010**

Class Awarded: **First Class with Distinction**

S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS
<b>I YEAR</b>											
1	APPLIED PHYSICS	18	34	52	4*	2	BASIC ELECTRONIC DEV.&CKTS.	17	55	72	4
3	C PRG. & DATA STRUCTURES	16	62	78	6	4	ELECTRICAL CIRCUIT ANALYSIS	17	67	84	6
5	ENGINEERING DRAWING	18	65	83	4	6	ENGLISH	17	40	57	4
7	MATHEMATICAL METHODS	19	52	71	6	8	MATHEMATICS - I	19	80	99	6
9	COMPUTER PROGRAMMING LAB	22	48	70	4	10	ELECTRONIC DEV.&CKTS.LAB	22	45	67	4
11	ENGG.WORKSHOP&IT WORKSHOP	23	48	71	4	12	ENGLISH LANG.COMM.SKILLS LAB	22	36	58	4
<b>II YEAR</b>											
1	ELECTRICAL MACHINES - I	20	47	67	4	1	CONTROL SYSTEMS	17	73	90	4
2	ELECTROMAGNETIC FIELDS	18	47	65	4	2	ELECTRICAL MACHINES - II	16	45	61	4
3	FLUID MECHANICS & HYDRAULIC MACHINE	18	54	72	4	3	ENVIRONMENTAL STUDIES	18	37	55	4
4	MATHEMATICS - III	19	69	88	4	4	LINEAR & DIGITAL IC APPLICATIONS	17	37	54	4
5	PULSE AND DIGITAL CIRCUITS	18	46	64	4	5	MANAGERIAL ECO. & FINANCIAL ANALYSIS	16	58	74	4
6	SWITCHING THEORY & LOGIC DESIGN	18	77	95	4	6	POWER SYSTEMS - I	18	48	66	4
7	ELECTRICAL CIRCUITS & SIMULATION (LAB)	24	46	70	2	7	ELECTRICAL MACHINES - I (LAB)	24	49	73	2
8	F M & H M (LAB)	24	49	73	2	8	IC & PULSE AND DIGITAL CIRCUITS (LAB)	21	42	63	2
<b>III YEAR</b>											
1	COMPUTER SYSTEM ORGANIZATION	18	48	66	4	1	DIGITAL SIGNAL PROCESSING	16	54	70	4
2	ELECTRICAL MACHINES-III	17	47	64	4	2	INSTRUMENTATION	17	57	74	4
3	ELECTRICAL MEASUREMENTS	16	60	76	4	3	MANAGEMENT SCIENCE	16	57	73	4
4	LINEAR SYSTEM ANALYSIS(NEW)	17	73	90	4	4	MICRO PROCESSORS AND MICRO CONTROL	13	42	55	4
5	POWER ELECTRONICS	18	37	55	4	5	SWITCH GEAR & PROTECTION	18	59	77	4
6	POWER SYSTEMS-II	20	42	62	4	6	VLSI DESIGN	18	29	47	4*
7	CONTROL SYSTEMS AND SIMULATION LAB	25	46	71	2	7	ADVANCED ENGLISH COMMUNICATIONS	20	44	64	2
8	ELECTRICAL MACHINES LAB - II	23	47	70	2	8	POWER ELECTRONICS AND SIMULATION LAB	24	46	70	2
<b>IV YEAR</b>											
1	ELECTRICAL DISTRIBUTION SYSTEMS	19	60	79	4	1	COMPREHENSIVE VIVA	0	98	98	2
2	HVDC TRANSMISSION	14	58	72	4	2	ADVANCED CONTROL SYSTEMS	18	61	79	4
3	NEURAL NETWORKS AND FUZZY LOGIC	19	33	52	4	3	DATABASE MANAGEMENT SYSTEMS	13	42	55	4
4	POWER SEMICONDUCTOR DRIVES	19	34	53	4	4	UTILIZATION OF ELECTRICAL ENERGY	18	50	68	4
5	POWER SYSTEM ANALYSIS	17	66	83	4	5	SEMINAR	50	-	50	2
6	POWER SYSTEM OPERATION AND CONTROL	19	68	87	4	6	INDUSTRY ORIENTED MINI PROJECT	-	49	49	2
7	ELECTRICAL MEASUREMENTS LAB	23	46	69	2	7	PROJECT WORK	40	159	199	10
8	MICROPROCESSORS AND MICROCONTROLLERS	25	49	74	2						

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Number of Credits registered for : **216 Credits** 4144 out of 5350 ( 77.46 %)

Aggregate Marks Secured for best :

Date of Declaration of Result : **May 2013**

(See overleaf for Instructions)

(\*Courses registered but not countered for calculation of aggregate)

18/7/2013

**CONTROLLER OF EXAMINATIONS**