JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

21046042410
RF437015

31046042360

440104668258 **College Code: 21 (B V R I T, NARSAPUR)**

Mr. Badda Hari Babu

S/o Mr. Badda Kutumba Rao

having fulfilled the academic requirements and passed the examination held during *April - 2012* in *First Class* has this day been admitted by the Executive Council to the Degree of

Bachelor of Technology

Electrical & Electronics Engineering

Given under the Seal of the University

H.T.No:**09215A0206**

Date: **09-11-2013**

DIRECTOR OF EVALUATION | REGISTRAR | VICE - CHANCELLOR

0ABJNT0215790

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET

BACHELOR OF TECHNOLOGY- ELECTRICAL & ELECTRONICS ENGINEERING

CMM. No. : C 0160113

Serial No. : 21046042410

Name : BADDA HARI BABU

Hall Ticket No. : 09215A0206

Year of Admission : 2009-2010

Name of the College : 21-B V R I T,NARSAPUR

Month & Year of Final Exam : April,2012

Class Awarded : FIRST CLASS

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	20	80	100			Maximum Marks in Lab	25	50	75	
	I YEAR										
	DIRECT ADMISSION INTO II-YEAR						**UNDER LATERAL ENTRY SCHEME**				
	I SEMESTER						**II SEMESTER**				
II YEAR											
1	MATHEMATICS - III	07	18	25*	0	1	MANAGERIAL ECO. & FINANCIAL ANALYSIS	12	60	72	4
2	FLUID MECHANICS & HYDRAULIC MACHINERY	12	51	63	4	2	ENVIRONMENTAL STUDIES	15	54	69	4
3	PULSE AND DIGITAL CIRCUITS	03	37	40*	4	3	LINEAR & DIGITAL IC APPLICATIONS	06	42	48	4
4	SWITCHING THOERY & LOGIC DESIGN	11	29	40	4	4	POWER SYSTEMS - I	15	48	63	4
5	ELECTROMAGNETIC FIELDS	06	50	56	4	5	ELECTRICAL MACHINES - II	12	50	62	4
6	ELECTRICAL MACHINES - I	06	34	40	4	6	CONTROL SYSTEMS	06	34	40	4
7	F M & H M (LAB)	20	48	68	2	7	IC & PULSE AND DIGITAL CIRCUITS (LAB)	20	32	52	2
8	ELECTRICAL CIRCUITS & SIMULATION (LAB)	19	39	58	2	8	ELECTRICAL MACHINES - I (LAB)	22	39	61	2
	I SEMESTER						**II SEMESTER**				
III YEAR											
1	COMPUTER SYSTEM ORGANIZATION	15	34	49	4	1	DIGITAL SIGNAL PROCESSING	13	46	59	4
2	ELECTRICAL MEASUREMENTS	19	56	75	4	2	MICROPROCESSORS & MICROCONTROLLERS	13	34	47	4
3	POWER SYSTEMS-II	16	28	44	4	3	MANAGEMENT SCIENCE	14	35	49	4
4	POWER ELECTRONICS	13	40	53	4	4	VLSI DESIGN	14	47	61	4
5	ELECTRICAL MACHINES-III	11	29	40	4	5	INSTRUMENTATION	18	40	58	4
6	LINEAR SYSTEMS ANALYSIS	13	28	41	4	6	SWITCH GEAR & PROTECTION	13	37	50	4
7	ELECTRICAL MACHINES LAB - II	20	41	61	2	7	ADVANCED ENGLISH COMMN. SKILLS (LAB)	21	47	68	2
8	CONTROL SYSTEMS AND SIMULATION LAB	12	37	49	2	8	POWER ELECTRONICS & SIMULATION (LAB)	19	41	60	2
	I SEMESTER						**II SEMESTER**				
IV YEAR											
1	NEURAL NETWORKS AND FUZZY LOGIC	18	29	47	4	1	UTILIZATION OF ELECTRICAL ENERGY	18	52	70	4
2	MICROPROCESSORS AND MICROCONTROLLERS (LAB)	21	41	62	2	2	COMPREHENSIVE VIVA	-	98	98	2
3	ELECTRICAL MEASUREMENTS (LAB)	23	45	68	2	3	PROJECT WORK#	39	155	194	10
4	POWER SEMICONDUCTOR DRIVES	15	54	69	4	4	ADVANCED CONTROL SYSTEMS	16	39	55	4
5	POWER SYSTEM ANALYSIS	18	43	61	4	5	PROGRAMMABLE LOGIC CONTROLLERS	18	53	71	4
6	POWER SYSTEM OPERATION AND CONTROL	20	33	53	4	6	SEMINAR	49	-	49	2
7	HVDC TRANSMISSION	16	30	46	4	7	INDUSTRY ORIENTED MINI PROJECT	-	48	48	2
8	NON-CONVENTIONAL SOURCES OF ENERGY	16	53	69	4						
							(# Project Internal= 40 , External=160)				

Number of Credits registered for : 168 Aggregate Marks Secured for best: 160

Aggregate Marks Secured : 2716 OUT OF 4250 (63.91%)

Date of Issue : July 6, 2012

(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'

(*Courses registered but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS