

# UTTARAKHAND TECHNICAL UNIVERSITY, DEHRADUN (INDIA)



STATEMENT OF MARKS 2008-09

S.No: 001552

B.Tech (Electrical & Electronics Engineering)  
FIRST YEAR

Name of Candidate : Ankit Barwal  
Father's Name : Yashpal Singh  
Name of Institution : Uttarakhand Institute of Technology Dehradun

Roll No : 08300108200  
Enrollment No. : 08300108200

1st Semester						2nd Semester									
Subject Code and Name		Maximum Marks		Marks Obtained		Subject Code and Name		Maximum Marks		Marks Obtained					
		Exam	Seat	Exam	Seat			Exam	Seat	Exam	Seat	Exam	Seat		
<b>THEORY</b>						<b>THEORY</b>									
TCY 101	Chemistry	100	50	130	52	43	95	YCS 201	Computer System Programming	100	50	150	67	36	103
TEC 101	Basic Electronics	100	50	150	35	43	90	YES 201	Environmental Studies	50	25	75	15	18	33
WE 101	Basic Electrical Engg	100	50	150	66	43	109	DH 201	Technical Writing	50	25	70	28	20	48
TRC 101	Professional Communication	50	25	75	41	17	59	MA 202	Mathematics	100	50	100	59	44	103
MA 101	Mathematics	100	50	150	57	38	95	MHE 201	Mechanical Engg	100	50	100	41	46	87
<b>PRACTICAL</b>						<b>PRACTICAL</b>									
PCY 151	Engineering Graphics	50	25	75	41	18	59	PCS 251	Computer Lab	25	12	50	22	21	43
PCY 151	Chemistry Lab	25	12	50	22	24	46	PME 251	Mechanical Lab	25	12	50	22	22	44
PEE 151	Basic Electrical & Electronics Lab	25	12	50	22	22	44	PHY 251	Physics Lab	25	12	50	23	22	45
PWE 151	Workshop Practice	25	12	50	23	22	45	Discipline	-	100	100	-	91	91	
<b>TOTAL</b>		375	325	600	375	270	649	CP	General Proficiency	-	100	100	-	85	85
								<b>TOTAL</b>		375	325	1100	360	453	813

Total No.	Carry Over Paper(s) (if any) Subject(s) Code	Grade Marks (if any)	Marks Obtained/Maximum Marks			Result
			1st Semester	2nd Semester	Grand Total	
			649/600	813/1100	1462/2000	Pass

Dated: 26/06/2010

Prepared By:

Checked By:

*[Signature]*  
Registrar

# UTTARAKHAND TECHNICAL UNIVERSITY, DEHRADUN (INDIA)



STATEMENT OF MARKS 2009-10

B.Tech (Electrical & Electronics Engineering)  
SECOND YEAR

SL.No: 000004

Name of Candidate

Ashu Kumar

Father's Name

Yashpal Singh

Name of Institution

Uttarakhand Institute of Technology Dehradun

Roll No: 09000004

Department: 09000004

## 2nd Semester

## 4th Semester

Subject Code and Name	Maximum Marks		Marks Obtained			Subject Code and Name	Maximum Marks		Marks Obtained			
	Thry	Prac	Thry	Prac	Total		Thry	Prac	Thry	Prac	Total	
<b>THEORY</b>												
EEE 201 Solid State Devices and Circuits	100	50	110	50	50	EEE 401 Electromagnetic Field Theory	100	50	100	45	41	86
EEE 204 Power & Digital Electronics	100	50	100	50	40	EEE 401 Electromechanical Design	100	50	100	50	40	90
EEE 203 Network Analysis & Synthesis	100	50	100	50	45	EEE 402 Power System Protection	100	50	100	50	40	90
EEE 202 Electrical Measurements and Measuring Instruments	100	50	100	41	50	EEE 403 Control & Electronic Engg Materials	100	50	100	50	40	90
EMA 201 Mathematics - II	100	50	100	50	40	EEE 404 Microprocessor	100	50	100	40	50	90
<b>PRACTICAL</b>												
EEE 202A Analog & Digital Experiments Lab 1	25	25	50	23	24	EEE 404A Electromechanical Expts	25	25	50	20	21	41
EEE 202B Network Lab	25	25	50	23	22	EEE 404B Microprocessor Lab	25	25	50	20	20	40
EEE 202C Electrical Measurements Lab	25	25	50	21	22	EEE 404C Electrical Simulation Lab	25	25	50	20	20	40
<b>TOTAL</b>	<b>170</b>	<b>85</b>	<b>340</b>	<b>200</b>	<b>200</b>	<b>TOTAL</b>	<b>170</b>	<b>85</b>	<b>340</b>	<b>200</b>	<b>200</b>	<b>400</b>

Index No.	Entry Over Page(s) (if any)	Subject Code	Grade Marked (if any)	Marks Obtained (Total marks 400)			Result
				2nd Semester	4th Semester	Grand Total	
				200/100	200/100	400/200	Pass

Dated: 11/05/2011

Prepared by: [Signature]

Checked by: [Signature]

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U.T. No. 0074765  
**UTTARAKHAND TECHNICAL UNIVERSITY, DEHRADUN (INDIA)**

STATEMENT OF MARKS

2022-23

Class: 1027402

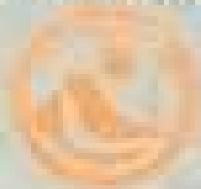
**B.Tech (Computer & Electronics Engineering)**  
**THIRD YEAR**

Name of Candidate: **Arif Sultan** Roll No.: **10274020000000000000**  
 Father's Name: **Muhammad Siraj** Department No.: **10274020000000000000**  
 Name of Institution: **Uttarakhand Institute of Technology, Dehradun**

S18 Semester				S19 Semester			
Subject Code and Name	Maximum Marks/Mark Obtained			Subject Code and Name	Maximum Marks/Mark Obtained		
	Total	Exam	Theory		Total	Exam	Theory
<b>THEORY</b>							
102 201 Analog Integrated Circuits	100	60	100	102 202 Analog and Digital Communication	100	60	100
102 202 Electromechanical Energy Conversion I	100	60	100	102 203 Power System Analysis	100	60	100
102 203 Control System	100	60	100	102 204 Power Electronics	100	60	100
102 204 Elements of Power System	100	60	100	102 205 High Voltage Engineering	100	60	100
102 205 Industrial Electronics & Modules of Microprocessor	100	60	100	102 207 Applied Instrumentation	100	60	100
<b>PRACTICAL</b>							
102 206 Laboratory Lab - II	50	30	50	102 206 Power Electronics Lab	50	30	50
102 207 Electromechanical Energy Conversion II Lab	50	30	50	102 208 Transformers Lab	50	30	50
102 208 Control System Lab	50	30	50	102 209 Seminar I	10	10	10
				102 209 Seminar II	10	10	10
				102 209 Seminar III	10	10	10
				102 209 Seminar IV	10	10	10
				102 209 Seminar V	10	10	10
				102 209 Seminar VI	10	10	10
				102 209 Seminar VII	10	10	10
				102 209 Seminar VIII	10	10	10
				102 209 Seminar IX	10	10	10
				102 209 Seminar X	10	10	10
				102 209 Seminar XI	10	10	10
				102 209 Seminar XII	10	10	10
				102 209 Seminar XIII	10	10	10
				102 209 Seminar XIV	10	10	10
				102 209 Seminar XV	10	10	10
				102 209 Seminar XVI	10	10	10
				102 209 Seminar XVII	10	10	10
				102 209 Seminar XVIII	10	10	10
				102 209 Seminar XIX	10	10	10
				102 209 Seminar XX	10	10	10
				102 209 Seminar XXI	10	10	10
				102 209 Seminar XXII	10	10	10
				102 209 Seminar XXIII	10	10	10
				102 209 Seminar XXIV	10	10	10
				102 209 Seminar XXV	10	10	10
				102 209 Seminar XXVI	10	10	10
				102 209 Seminar XXVII	10	10	10
				102 209 Seminar XXVIII	10	10	10
				102 209 Seminar XXIX	10	10	10
				102 209 Seminar XXX	10	10	10
				102 209 Seminar XXXI	10	10	10
				102 209 Seminar XXXII	10	10	10
				102 209 Seminar XXXIII	10	10	10
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				102 209 Seminar XXXVI	10	10	10
				102 209 Seminar XXXVII	10	10	10
				102 209 Seminar XXXVIII	10	10	10
				102 209 Seminar XXXIX	10	10	10
				102 209 Seminar XL	10	10	10
				102 209 Seminar XLI	10	10	10
				102 209 Seminar XLII	10	10	10
				102 209 Seminar XLIII	10	10	10
				102 209 Seminar XLIV	10	10	10
				102 209 Seminar XLV	10	10	10
				102 209 Seminar XLVI	10	10	10
				102 209 Seminar XLVII	10	10	10
				102 209 Seminar XLVIII	10	10	10
				102 209 Seminar XLIX	10	10	10
				102 209 Seminar L	10	10	10
				102 209 Seminar LI	10	10	10
				102 209 Seminar LII	10	10	10
				102 209 Seminar LIII	10	10	10
				102 209 Seminar LIV	10	10	10
				102 209 Seminar LV	10	10	10
				102 209 Seminar LVI	10	10	10
				102 209 Seminar LVII	10	10	10
				102 209 Seminar LVIII	10	10	10
				102 209 Seminar LIX	10	10	10
				102 209 Seminar LX	10	10	10
				102 209 Seminar LXI	10	10	10
				102 209 Seminar LXII	10	10	10
				102 209 Seminar LXIII	10	10	10
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				102 209 Seminar LXV	10	10	10
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				102 209 Seminar LXXXXXIV	10	10	10
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				102 209 Seminar LXXXXXVII	10	10	10
				102 209 Seminar LXXXXXVIII	10	10	10
				102 209 Seminar LXXXXXIX	10	10	10
				102 209 Seminar LXXXXXX	10	10	10
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				102 209 Seminar LXXXXXXXVI	10	10	10
				102 209 Seminar LXXXXXXXVII	10	10	10
				102 209 Seminar LXXXXXXXVIII	10	10	10
				102 209 Seminar LXXXXXXXIX	10	10	10
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				102 209 Seminar LXXXXXXXII	10	10	10
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				102 209 Seminar LXXXXXXXVI	10	10	10
				102 209 Seminar LXXXXXXXVII	10	10	10
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				102 209 Seminar LXXXXXXXIII	10	10	10
				102 209 Seminar LXXXXXXXIV	10	10	10
				102 209 Seminar LXXXXXXXV	10	10	10
				102 209 Seminar LXXXXXXXVI	10	10	10
				102 209 Seminar LXXXXXXXVII	10	10	10

# UTTARAKHAND TECHNICAL UNIVERSITY, DEHRADUN (INDIA)

Serial No. 204256



## STATEMENT OF MARKS 2011-12 B.Tech (Electrical & Electronics Engineering) FINAL YEAR

URTE/11/011

Name of Candidate : ANKIT BARTHA  
 Father's Name : TASHMAL SINGH  
 Name of Institution : Uttarakhand Institute of Technology Dehradun

Roll No. : 00200100004  
 Enrollment No. : 00200100004

7th Semester						8th Semester					
Subject Code and Name		Maximum Marks		Marks Obtained		Subject Code and Name		Maximum Marks		Marks Obtained	
		Exam	Theor.	Exam	Theor.			Exam	Theor.	Exam	Theor.
<b>THEORY</b>						<b>THEORY</b>					
TDC 704 Digital Signal Processing		100	50	68	47	TDC 800 Telemetry & Data Transmission		100	50	68	47
TEE 703 Neural Networks & Fuzzy Logic		100	50	42	47	TEE 802 Bio-Instrumentation		100	50	48	47
TEE 702 Electric Drives		100	50	43	47	TEE 800 Power Converter Application		100	50	72	47
TEE 704 Switch Gear & Protection		100	50	58	47	TEE 803 Operation and Control in Power System		100	50	51	47
TDC 818 Consumer Electronics		100	50	50	48						
<b>PRACTICAL</b>						<b>PRACTICAL</b>					
PEC 733 Digital Signal Processing Lab		50	25	27	22	PEC 803 Telemetry Lab		50	25	22	24
PEE 701 Power System Lab		50	25	21	24	PEE 802 Project		200	100	100	95
PEE 703 Industrial Training & Seminar		--	50	--	45	GP - General Proficiency		--	100	100	--
PEE 704 Project		--	50	--	47			--	50	50	--
<b>TOTAL</b>		<b>1200</b>	<b>600</b>	<b>488</b>	<b>348</b>	<b>TOTAL</b>		<b>1475</b>	<b>745</b>	<b>745</b>	<b>670</b>

Carry Over Paper(s)		Grade Marks (if any)	Marks Obtained/Maximum Marks				Grand Total	Final Result
Total No.	Subject(s) Code		First Year (20%)	Second Year (30%)	Third Year (70%)	Fourth Year (100%)		
			364/1820	730/2400	1074/1500	1964/5000	3624/5000	Pass

Date: 12/07/2012

Prepared by:

Checked:

Registrar  
 Uttarakhand Technical University  
 Dehradun  
 Uttarakhand